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2811
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Mallish

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Noriaki Sakamoto et al.
Serial No. : 09/810,105 ✓
Filed : March 16, 2001 ✓
Title : SEMICONDUCTOR DEVICE

Art Unit : 2811
Examiner : N. Parekh

Commissioner for Patents
Washington, D.C. 20231

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AMENDMENT

In response to the action mailed January 7, 2002, please amend the application as follows:

In the specification:

Please replace the paragraph beginning at page 7, line 1 and ending at page 8, line 5 with the following rewritten paragraph:

--Brief Description of the Drawings

A1

Fig. 1 is a view for explaining a semiconductor device according to this invention.

Fig. 2 is a view for explaining a method of manufacturing a semiconductor device according to this invention.

Fig. 3 is a view for explaining a method of manufacturing a semiconductor device according to this invention.

Fig. 4 is a view for explaining a method of manufacturing a semiconductor device according to this invention.

Fig. 5 is a view for explaining a method of manufacturing a semiconductor device according to this invention.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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April 5, 2002

N. Parekh

N. Parekh